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APPLICANTS	<del>'</del>	المجموع في المستوا			
Kazutaka Mori, Norio Hasegawa Tsuneo Terasay	kubunji, JAPAN; Kokubunji, JAPAN; a, Hinode, JAPAN; va, Ome, JAPAN; va, Tokyo, JAPAN;	15	;		
CONTINUING DATA	4	•	•		
** FOREIGN APPLICA JAPAN 2000-30	NTONS ************************************	SK			
IF REQUIRED, FORE 11/08/2001	GN FILING LICENSE	GRANTED			
Foreign Priority claimed 35 USC 119 (a-d) conditions met Vorified and Acknowledged	Vyes Ono O Met after Allowance Minut Signature Unit	STATE OR COUNTRY JAPAN	SHEETS TOTAL DRAWING CLAI 75 25		
ADDRESS 020457					
NTLE			->> See An	end ment 1/21/05	
Fabrication method of	semiconductor integrate	ed circuit device			
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			1.16 Fees (	1.16 Fees ( Filing )	
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